The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (original): A method for providing multimedia functionality in a homogeneous multiprocessor environment comprising the steps of:

 queuing tasks;

 identifying available processing resources in the homogeneous multiprocessor environment;

 allocating the available processing resources among the tasks;

 providing to the available processing resources functional programs and initial data corresponding to the tasks;

 performing the tasks using the available processing resources to produce resulting data.

Claim 2 (original): The method of claim 1 wherein a plurality of processors of the homogeneous multiprocessor environment are capable of executing a first instruction of a first instruction set and a second instruction of a second instruction set.

Claim 3 (original): The method of claim 2 wherein the first instruction and the second instruction share an identical bit pattern but perform different operations.

Claim 4 (original): The method of claim 3 wherein a first processor of the plurality of processors executes an input/output kernel program, the input/output kernel program including a first portion expressed using the first instruction set and a second portion expressed using the second instruction set.

Claim 5 (original): The method of claim 3 further comprising the step of:

 converting a functional program of the functional programs expressed using the first instruction set to an equivalent functional program expressed using the second instruction set.

Claim 6 (currently amended): The method of claim 3 ~~further~~ wherein the tasks comprise:

 x86 processing;

 graphic image processing;

video processing;

audio processing; and

communication processing.

Claim 7 (original): The method of claim 3 further comprising the step of:

receiving the initial data from a first input/output device.

Claim 8 (original): The method of claim 3 further comprising the steps of:

passing the resulting data to a first input/output device.

Claim 9 (original): The method of claim 8 wherein the step of passing the resulting data to the first input/output device further comprises the step of:

passing the resulting data through an intermediary device, wherein the intermediary device is coupled to the first input/output device and to a second input/output device.

Claim 10(original): The method of claim 9 wherein the step of passing the resulting data through an intermediary device, wherein the intermediary device is coupled to the first input/output device and to a second input/output device further comprises the step of:

automatically adapting to a reallocation of the available processing resources among the tasks.

Claim 11 (original): The method of claim 8 wherein the step of passing the resulting data to a first input/output device further comprises the step of:

passing the resulting data to a mixed-signal device.

Claim 12 (original): The method of claim 3 wherein the step of allocating the available processing resources among the tasks is dynamically adjusted.

Claim 13 (original): Apparatus comprising:

a plurality of processors coupled to a bus;

an input/output interface coupled to the bus;

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